SET - 1

[8M]

[7M]

III B. Tech I Semester Regular Examinations, February-2022 MICROPROCESSORS AND MICROCONTROLLERS

(Electronics and Communication Engineering)

Time: 3 hours Max. Marks: 75

Answer any **FIVE** Questions **ONE** Question from **Each unit** All Questions Carry Equal Marks

UNIT-I

- 1. a) Explain the functionality of pins used in the following modes of [8M] 8086 microprocessor: i) Minimum mode ii) Maximum mode.
 - b) What is a microprocessor? Explain the brief history of evolution [7M] of microprocessor.

(OR)

- 2. a) Discuss the interrupts and interrupt response of 8086. [8M]
 - b) Write a program to add a data byte located at offset 0500H in [7M] 2000H segment to another data byte available at 0600H in the same segment and store the result at 0700H in the same segment.

UNIT-II

- 3. a) Mention any four different types of addressing modes of 8086 [8M] instruction set? And explain them.
 - b) Write an assembly language program for a 16-bit increment and [7M] that will not affect the contents of the accumulator.

(OR

- 4. a) Discuss the assembler directives of 8086 with examples.
 - b) Write an 8086 ALP to convert ASCII to BCD number.

UNIT-III

- 5. a) Describe the operational modes of 8255 programmable [8M] peripheral interface.
 - b) Justify how D/A and A/D interfacing done with 8086 with an [7M] application.

(OR)

- 6. a) Explain the 8251 USART with neat block diagram and its mode [8M] word, command word and status word.
 - b) List the applications of software and hardware interrupt and [7M] explain about 8259.

1 of 2

Code No: R1931042 R19

SET - 1

UNIT-IV

- 7. a) Explain interfacing of Keyboard/Display with 8051 [8M] microcontroller.
 - b) Compare Microprocessor and Microcontroller.

[7M]

(OR)

- 8. a) Name the special function registers available in 8051 and list the [8M] features of 8051 microcontroller.
 - b) Write an ALP using 8051 instructions to receive bytes of data [7M] serially and put them in PI. Set the baud rate at 4800, 8-bit data and 1 stop bit.

UNIT-V

- 9. a) Explain Cortex M3 processor system block diagram. [8M]
 - b) Explain modes of operation and execution in programmer's [7M] model.

(OR)

- 10. a) Explain the functional description of the nested vectored [8M] interrupt controller.
 - b) With neat block diagram explain the functions of ARM processor. [7M] Compare it with PIC and list out the major differences.

2 of 2