### Code No: R194104K

Set No. 1

### IV B. Tech I Semester Regular Examinations, November – 2022 EMBEDDED SYSTEMS (EXCEPT FOR ECE) (Open Elective)

Time: 3 hours Max. Marks: 75

# Answer any FIVE Questions ONE Question from Each unit

		All Questions Carry Equal Marks  *****	
		Unit - I	
1	a) b)	Define sensors? Explain the I/O subsystem. What is non-operational quality attributes? Explain the important non-operational Quality attributes to be considered in any embedded system design.	[7] [8]
		(OR)	
2	a)	Write the details about the embedded hardware units and devices in a system.	[7]
	b)	Compare the application-specific and domain-specifics of an embedded	[8]
		system.  Unit - II	
3	a)	Explain briefly about watchdog timer.	[7]
	b)	What are the various serial communication devices used in an	[8]
	٥,	Embedded Hardware? Explain any one of them.  (OR)	[~]
4	a)	Explain about Timer and counting devices in Embedded Hardware.	[7]
	b)	Explain about watchdog Timer and real time clock.	[8]
		Unit - III	
5	a)	Discuss the Mixing Assembly with high level language and mixing high level language with assembly.	[7]
	b)	Explain any one of Embedded firmware design approaches in detail. (OR)	[8]
6	a)	With the help of appropriate diagrams explain the working of DMA.	[7]
	b)	Write a note on C versus embedded C and compiler versus cross compiler.	[8]
		Unit - IV	
7	a)	What is deadlock? List and explain different conditions favoring a deadlock situation.	[7]
	b)	Compare various Task scheduling algorithms in RTOS.	[8]

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8	a)	Explain the different multitasking models in operating system context	[7]
	b)	Explain the fundamental issues in hardware software co-design.	[8]
		Unit - V	
9	a)	Draw and explain the integrated embedded system development environment.	[7]
	b)	What is ROM emulator? Explain ICE based debugging in detail. (OR)	[8]
10	a)	Explain in detail Translation tools-Pre-processors.	[7]
	b)	Discuss Simulators and Laboratory tools in details.	[8]

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Set No. 2

#### IV B. Tech I Semester Regular Examinations, November – 2022

# EMBEDDED SYSTEMS (EXCEPT FOR ECE)

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Time: 3 hours Max. Marks: 75 Answer any FIVE Questions ONE Question from Each unit All Questions Carry Equal Marks 1 Unit - I Explain the different classifications of Embedded Systems, Give an [7] example for each. What is Actuator? Explain its role in Embedded System Design? b) [8] Illustrate with an example. (OR) What is embedded firm ware explain. 2 a) [7] Explain the purpose of embedded system. [8] b) Unit - II 3 a) What is combinational circuit? Explain with example. [7] Explain briefly about wireless devices. b) [8] (OR) Explain the sequence of operations for communicating with an I2c slace [7] 4 a) device. Explain the purpose of (i) Counting Device and (ii) Real Time Clock in [8] b) an embedded system. Unit - III What is Device driver? Explain about device driver programming. 5 a) [7] List out the differences between an architecture specific device driver [8] b) and a generic device driver. (OR) Explain the conversion process from source file to object file [7] 6 a) translation. b) Explain briefly about interrupt servicing mechanism. [8] **Unit - IV** Explain how thread and process are used in an embedded system. 7 a) [7] Differentiate between Hardware and Software Co-Design with all the [8] salient features of them. (OR)

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a)	Define process? Draw the processor state transition diagram and explain	[7]
b)	Define message passing? Explain how the message passing is classified.	[8]
a)		[7]
b)	What are the different techniques available for embedded firmware	[8]
	(OR)	
a)	What is a monitor program? Explain role in embedded firmware debugging.	[7]
b)	Explain the advantages and limitations of simulator based debugging.	[8]
	b) a) b) a)	it. b) Define message passing? Explain how the message passing is classified.  Unit - V  a) Explain the format of Hex records in an Intel Hex file. b) What are the different techniques available for embedded firmware debugging? Explain them in detail.  (OR)  a) What is a monitor program? Explain role in embedded firmware debugging.

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Set No. 3

### IV B. Tech I Semester Regular Examinations, November – 2022 EMBEDDED SYSTEMS (EXCEPT FOR ECE)

(Open Elective)

Time: 3 hours Max. Marks: 75 Answer any FIVE Questions ONE Question from Each unit All Questions Carry Equal Marks \*\*\*\* 1 Unit - I What are the different types of memories used in Embedded System design? [7] a) Explain the role of each. Explain the difference between Embedded Systems and General Computing [8] b) Systems. (OR) Write and explain the classification of embedded systems 2 a) [7] Explain the onboard communication interfaces. [8] b) Unit - II Explain the role of Watchdog timer in embedded system. 3 a) [7] Compare the data transfer using serial and parallel port devices along with [8] b) their advantages and disadvantages. What is Latch? Draw and explain the latch. 4 a) [7] Explain briefly about parallel device ports. [8] b) Unit - III Write and explain the drawbacks of assembly language based development. 5 a) [7] What is Interrupt? Explain multiple interrupts with examples. b) [8] (OR) Explain the advantages and disadvantages of high level language based 6 a) [7] embedded firmware development. What is interrupt? Explain its role in embedded application development. b) [8] Unit - IV 7 Discuss how ICE is useful for testing an Embedded System with neat [7] a) diagram. Explain the architecture of device drivers. [8] b) (OR) 8 Write and explain the basic functions of real time kernel. [7] a) Explain data flow graph and state machine model in embedded design. [8] b)

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		Unit - V	
		Explain in detail about different files generated during the cross compilation of an Embedded C file.	[7]
	b)	Explain the various details held by a Map file generated during the process of cross-compiling an embedded C project.  (OR)	[8]
10	a)	Draw the compilation diagram and explain it.	[7]
	b)	Explain in detail about below terms: i) Interpreters ii) Simulator	[8]

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Set No. 4

### IV B. Tech I Semester Regular Examinations, November – 2022 EMBEDDED SYSTEMS (EXCEPT FOR ECE)

(Open Elective)

Time: 3 hours Max. Marks: 75 Answer any FIVE Questions ONE Question from Each unit All Questions Carry Equal Marks Unit - I 1 a) Write the history of embedded system. [7] Differentiate RISC and CISC. [8] b) (OR) 2 a) Distinguish between a sensor and an actuator. Explain their role in an [7] embedded system with suitable examples. Explain about application specific embedded system with suitable example. [8] b) Unit - II Discuss Real time clock with respect to an Embedded Hardware. 3 [7] a) b) Compare the operation of ZigBee and Wi-Fi networks. [8] (OR) What are serial communication devices? Explain. 4 a) [7] Explain briefly about watchdog timer. b) [8] **Unit - III** Explain the advantages of assembly language based development. 5 [7] a) Write a note on C versus embedded C and compiler versus cross compiler. b) [8] (OR) 6 a) Discuss about compiler and cross compiler with respect to Embedded [7] Firmware. Explain structure in the 'Embedded-C' programming context. Explain the [8] b) significance of structure over normal variables. **Unit - IV** 7 Explain the important Hardware Software Tradeoffs in Hardware Software [7] a) Partitioning. Explain how to choose an RTOS. [8] b)

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		(OR)	
8	a)	Write notes on Embedded software development-process.	[7]
	b)	Explain in detail about Boundary scan.	[8]
		Unit - V	
9	a)	Explain various elements of an embedded system development environment.	[7]
	b)	Explain how the compiling needs of an embedded system are different from that of general purpose computer with suitable examples.  (OR)	[8]
10	a)	Differentiate static and dynamic testing.	[7]
	b)	Compare various Laboratory tools used for embedded system implementation and testing.	[8]