R19

Code No: R194104K

Set No. 1

IV B.Tech I Semester Advance Supplementary Examinations, March - 2023

EMBEDDED SYSTEMS

(EXCEPT FOR ECE)

(Open Elective)

Time: 3 hours Max. Marks: 75

Answer any FIVE Questions
ONE Question from Each unit
All Questions Carry Equal Marks

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	UNIT-I	
a)	Discuss in detail any three characteristics embedded system	[7]
b)	What is Domain-specific embedded system and explain with example	[8]
	(OR)	
a)	What is the role of embedded system present life? Discuss major application	
	areas of embedded systems.	[7]
b)		
	ii) reliability	[8]
	IINIT-II	
a)		[7]
		[8]
0)		[~]
		[15]
	UNIT-III	
a)	Define an interrupt and Classify it.	[7]
b)		[8]
	Discuss in detail about Embedded Firmware design approaches	[15]
	UNIT-IV	
a)		[8]
b)		[7]
	(OR)	
	Explain in detail about any two non pre-emptive scheduling algorithms	[15]
	IINIT-V	
a)		[7]
		[8]
a)	Compare various Laboratory tools used for embedded system implementation	
-	and testing	[7]
b)	With neat sketches of diagram explain about the interpretation.	[8]
	b) a) b) a) b) a) b) a) b) a) b) a) b)	a) Discuss in detail any three characteristics embedded system b) What is Domain-specific embedded system and explain with example (OR) a) What is the role of embedded system present life? Discuss major application areas of embedded systems. b) Discuss below Operational Quality attributes of embedded systems i) Safety ii) reliability UNIT-II a) What are serial communication devices? Explain. b) What are the modes of UART serial bit connected to RS232 port? (OR) Explain different types of Wireless devices UNIT-III a) Define an interrupt and Classify it. b) Explain briefly about Mixing Assembly and high level language (OR) Discuss in detail about Embedded Firmware design approaches UNIT-IV a) What are the difference between OS and RTOS? b) Define Tasks, Process and Thread (OR) Explain in detail about any two non pre-emptive scheduling algorithms UNIT-V a) Draw and explain the integrated embedded system development environment b Explain the advantages and limitations of simulator based debugging (OR) Compare various Laboratory tools used for embedded system implementation and testing